

REMARKS

The Rejections under 35 USC § 103(a)

The pending claims have been rejected under 35 U.S.C. § 103(a) as being unpatentable over various combinations of Korean Patent Application Publication No. 1020020078535 to *Shin* (“*Shin*”), U.S. Patent Application Publication No. 2001/0026125 to *Yamazaki et al.* (“*Yamazaki 1*”), and U.S. Patent No. 6,013,930 to *Yamazaki et al.* (“*Yamazaki 2*”). Applicant respectfully traverses, noting that none of the references, singly or in combination, discloses every element of the claims as amended. More specifically, none discloses a pixel electrode disposed on the same layer as a data wire, or a partition disposed directly on both a data wire and a pixel electrode.

First, Examiner asserts that the drain electrode 175 of *Shin* (*Shin*, Abstract) corresponds to Applicant’s recited “pixel electrode” (Office Action, p. 2). This is, respectfully, incorrect. A drain electrode is not a pixel electrode, nor would one of ordinary skill in the art consider a drain electrode to be a pixel electrode. The only structure of *Shin* that would even remotely be considered a pixel electrode is the anode electrode 200. However, this anode electrode 200 is not shown as being disposed on the same layer as any data wire (e.g., *Shin*, p. 7-7).

Neither *Yamazaki 1* nor *Yamazaki 2* cures this deficiency in *Shin*, as neither appears to disclose any pixel electrode on the same layer as any data wire. Accordingly, claim 1 is patentable over each of these references, singly or in combination, for at least the reason that it recites “a pixel electrode disposed on the same layer as the data wire.”

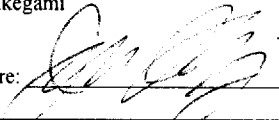
Second, none of the references discloses a partition disposed directly on both a data wire and a pixel electrode. Even if Examiner is correct that the protective layer 210 of *Shin* is a “partition” (which has not been shown), the protective layer 210 is at most disposed directly on an anode electrode 200 and third interlayer dielectric 180, not on a pixel electrode and data wire (e.g., *Shin*, p. 7-7). Neither *Yamazaki 1* nor *Yamazaki 2* cures this deficiency in *Shin*, as neither appears to disclose any partition that is disposed directly on both a data wire and a pixel electrode. In particular, *Yamazaki 1* at most discloses a supporting bank 41a directly on a pixel electrode 40 and second interlayer insulating film 39, rather than on a pixel electrode and data wire (e.g., FIG. 6). *Yamazaki 2* does not appear to disclose any partition at all, let alone one that is disposed directly on both a data wire and a pixel electrode. Accordingly, claim 1 is patentable over each of these references, singly or in combination, for at least the

additional reason that it recites "a partition disposed directly on both the data wire, and the pixel electrode."

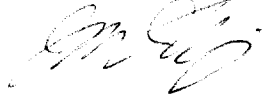
The remaining pending claims each depend from claim 1, and are thus also patentable over each of the above references for at least the same reasons as above.

CONCLUSION

Based on the foregoing, claims 1-6 and 10-13 are now in condition for allowance. The Director is hereby authorized to charge any additional fees, or credit any overpayment, to Deposit Account No. 50-5029. Please telephone the undersigned attorney at (408) 331-1671 if there are any questions.

<p>CERTIFICATE OF EFS-WEB TRANSMISSION</p> <p>Certificate of Transmission: I hereby certify that this correspondence is being transmitted to the United States Patent and Trademark Office (USPTO) via the USPTO's EFS-Web electronic filing system on December 22, 2009.</p> <p>Typed or printed name of person signing this certificate:</p> <p>Jon Y. Ikegami</p> <p>Signature: </p>
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